

40G QSFP+ to 4xDLC Breakout Active Optical Cable (AOC) **Hot Pluggable, 850nm VCSEL, MMF 1~100M, DDM**

Part Number: FAOC-40G-QPDP-xxx-xx



Overview

FAOC-40G-QPDP-xxx-xx 40G QSFP+ to 4xDLC+ Active Optical Cables (AOC) are direct-attach fiber assemblies with QSFP+ and Duplex LC form factor. The AOC utilize multi-mode fiber with 850nm VCSEL and PIN PD. It is used for 1x40GBASE-SR4 to 4x10GBASE-SR application. They are suitable for 1~100 meters distances and offer a cost-effective way for very high port density connections.

Applications

- 40GBASE-SR4 Ethernet to 4x10G BASE-SR Ethernet Link
- Data Center & Storage
- Datacom / Telecom Switch & Router

Features

- Compatible with IEEE802.3ba 40GBASE-SR4
- Compliant with SFF-8436 QSFP+ MSA
- 4 independent full-duplex channels
- 1x40GBASE-SR4 to 4x10GBASE-SR
- Up to 10.3125Gbps data rate per channel
- Hot Pluggable
- 850nm VCSEL array transmitter
- Duplex LC connector
- 2-wire interface for management and diagnostic monitor compliant with SFF-8436, SFF-8636
- Single 3.3V power supply
- Link distance up to 100m over MM OM3 fiber
- Low power consumption <1.5W
- RoHS Compliant



Absolute Maximum Ratings

Parameters	Symbol	Min.	Max.	Unit
Storage Temperature	T _{ST}	-20	+85	°C
Storage Relative Humidity	RH	5	85	%
Supply Voltage	V _{CC}	-0.5	+3.6	V

Recommended Operating Conditions

Parameters		Symbol	Min.	Typ.	Max.	Unit	Note
Case Operating Temperature		T _{OP}	0	-	+70	°C	
Supply Voltage		V _{CC}	+3.13	+3.3	+3.47	V	
Data Rate, per Lane		DR		10.3125		Gb/s	
Bit Error Rate		BER			10 ⁻¹²		1
Supply Current, per QSFP+		I _{CC}			450	mA	
Power Consumption, per QSFP+		P			1.5	W	
Power-On Initialization Time					2000	ms	2
Minimum Cable Bending Radius			30			mm	
ModSelL	Module Select	V _{OL}	V _{EE} -0.3		0.4	V	
	Module Unselect	V _{OH}	2.0		V _{CC} +0.3	V	
LPMode	Low Power Mode	V _{IL}	V _{EE} -0.3		0.8	V	
	Normal Operation	V _{IH}	2.0		V _{CC} +0.3	V	
ResetL	Reset	V _{IL}	V _{EE} -0.3		0.8	V	
	Normal Operation	V _{IH}	2.0		V _{CC} +0.3	V	
ModPrsL	Normal Operation	V _{OL}	V _{EE} -0.3		0.4	V	
IntL	Interrupt	V _{OL}	V _{EE} -0.3		0.4	V	
	Normal Operation	V _{OH}	2.0		V _{CC} +0.3	V	

Note1: Measured with a PRBS 231-1 test pattern @10.3125Gbps

Note2: Power-on Initialization Time is the time from when the power supply voltages reach and remain above the minimum recommended operating supply voltages to the time when the module is fully functional.



QSFP+ Transmitter Electro-optical Characteristics

V_{CC} = 3.13V to 3.47V, T_{OP} = 0 °C to 70 °C

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Average Optical Power, per Lane	P _{AVG}	-6.0		+2.4	dBm	1
Optical Center Wavelength	λ_c	830	850	870	nm	
Spectral Width (RMS)	$\Delta\lambda$			0.6	nm	
Optical Extinction Ratio	ER	3			dB	
Differential Input Impedance	Z _{IN}	90	100	110	Ω	
Differential Data Input Swing	V _{IN}	200		1000	mV	

Note1: The interconnected 10G-SR modules should have receiver overload up to +2.4 dBm or work with MMF Attenuator to control input optical power under overload threshold.

QSFP+ Receiver Electro-optical Characteristics

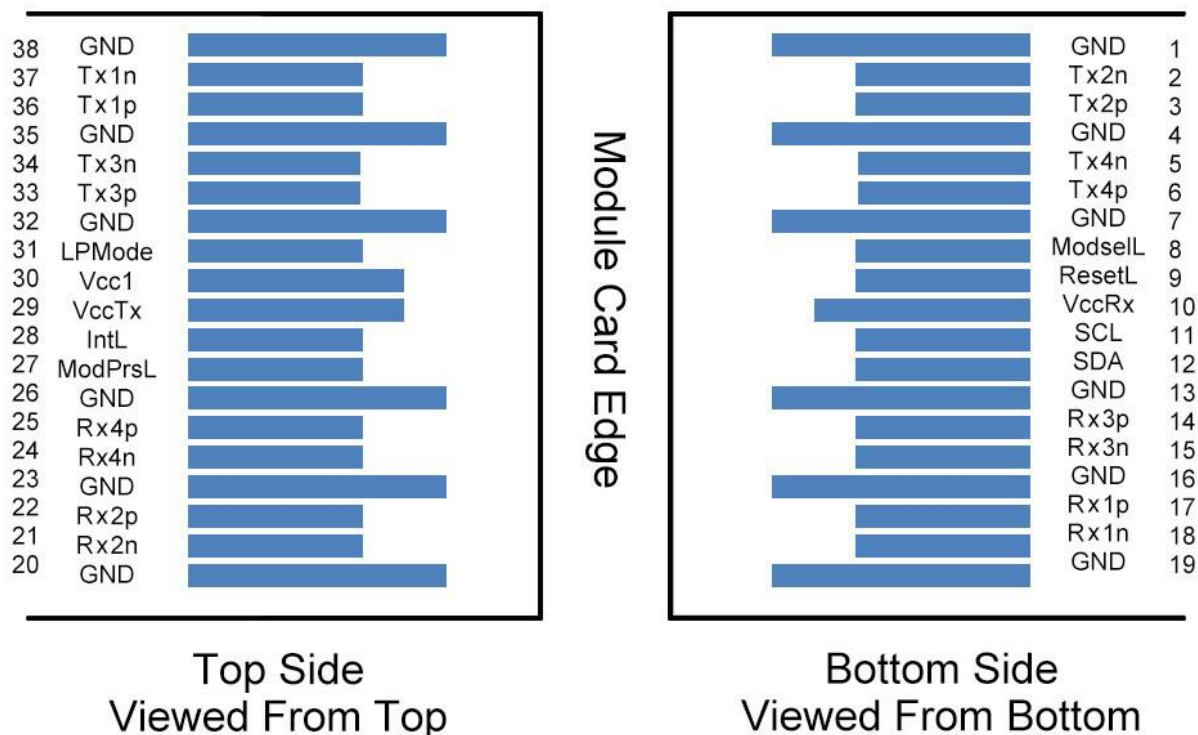
V_{CC} = 3.13V to 3.47V, T_{OP} = 0 °C to 70 °C

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Optical Center Wavelength	λ_c	840	850	860	nm	
Overload, per Lane	P _{Rx-OVL}	+2.4			dBm	
Receiver Sensitivity, per Lane (OMA)	P _{Rx-OMA}			-10.2	dBm	1
Differential Output Impedance	Z _{OUT}	90	100	110	Ω	
Differential Data Output Swing	V _{OUT}	200		1000	mV	

Note1: Measured with conformance test signal at receiver input for BER= 1x10⁻¹².



QSFP+ Pin Assignment



QSFP+ Pin Description

Pin	Logic	Name	Function / Description
1		GND	Module Ground
2	CML-I	Tx2n	Transmitter Inverted Data Input
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input
4		GND	Module Ground
5	CML-I	Tx4n	Transmitter Inverted Data Input
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input
7		GND	Module Ground
8	LVTLL-I	ModSelL	Module Select
9	LVTLL-I	ResetL	Module Reset
10		VccRx	+3.3V Power Supply Receiver
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data



13		GND	Module Ground
14	CML-O	Rx3p	Receiver Non-Inverted Data Output
15	CML-O	Rx3n	Receiver Inverted Data Output
16		GND	Module Ground
17	CML-O	Rx1p	Receiver Non-Inverted Data Output
18	CML-O	Rx1n	Receiver Inverted Data Output
19		GND	Module Ground
20		GND	Module Ground
21	CML-O	Rx2n	Receiver Inverted Data Output
22	CML-O	Rx2p	Receiver Non-Inverted Data Output
23		GND	Module Ground
24	CML-O	Rx4n	Receiver Inverted Data Output
25	CML-O	Rx4p	Receiver Non-Inverted Data Output
26		GND	Module Ground
27	LVTLL-O	ModPrsL	Module Present
28	LVTLL-O	IntL	Interrupt
29		VccTx	+3.3V Power Supply Transmitter
30		Vcc1	+3.3V Power Supply
31	LVTLL-I	LPMODE	Low Power Mode
32		GND	Module Ground
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input
35		GND	Module Ground
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input
38		GND	Module Ground

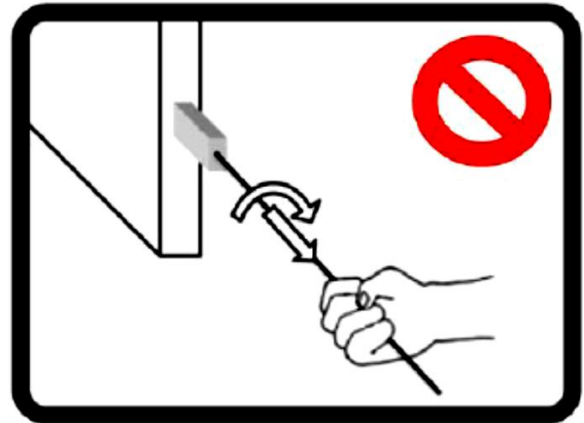
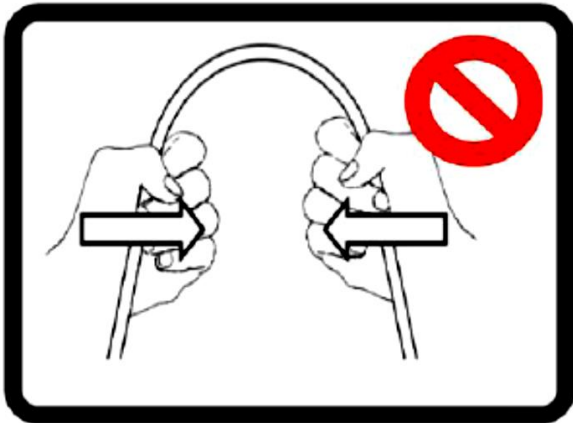
Note1: GND is the symbol for signal and supply (power) common for QSFP modules. All are common within the QSFP module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground lane.

Note2: VccRx, Vcc1 and VccTx are the receiver and transmitter power suppliers and shall be applied concurrently. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.



Handling

Care should be taken to restrict exposure to the conditions defined in the Absolute Maximum Ratings and Recommended Operating Conditions. Put the product in an even and stable location. If the product falls down or drops, it may cause an injury or malfunction. The cable must not be subject to extreme bends during installation or while in operation. If you bend the cable at a radius less than the cable minimum bend radius, then the cable may get damaged. Don't twist or pull by force ends of the cable, which might cause malfunction. In addition, the bending direction should be perpendicular to the flat surface of the ribbon cable. Please do not bend or kink the cable in lateral directions of flat surface of the ribbon.





QSFP+ Digital Diagnostic Functions

As defined by the QSFP+ MSA, Ficer's QSFP+ transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

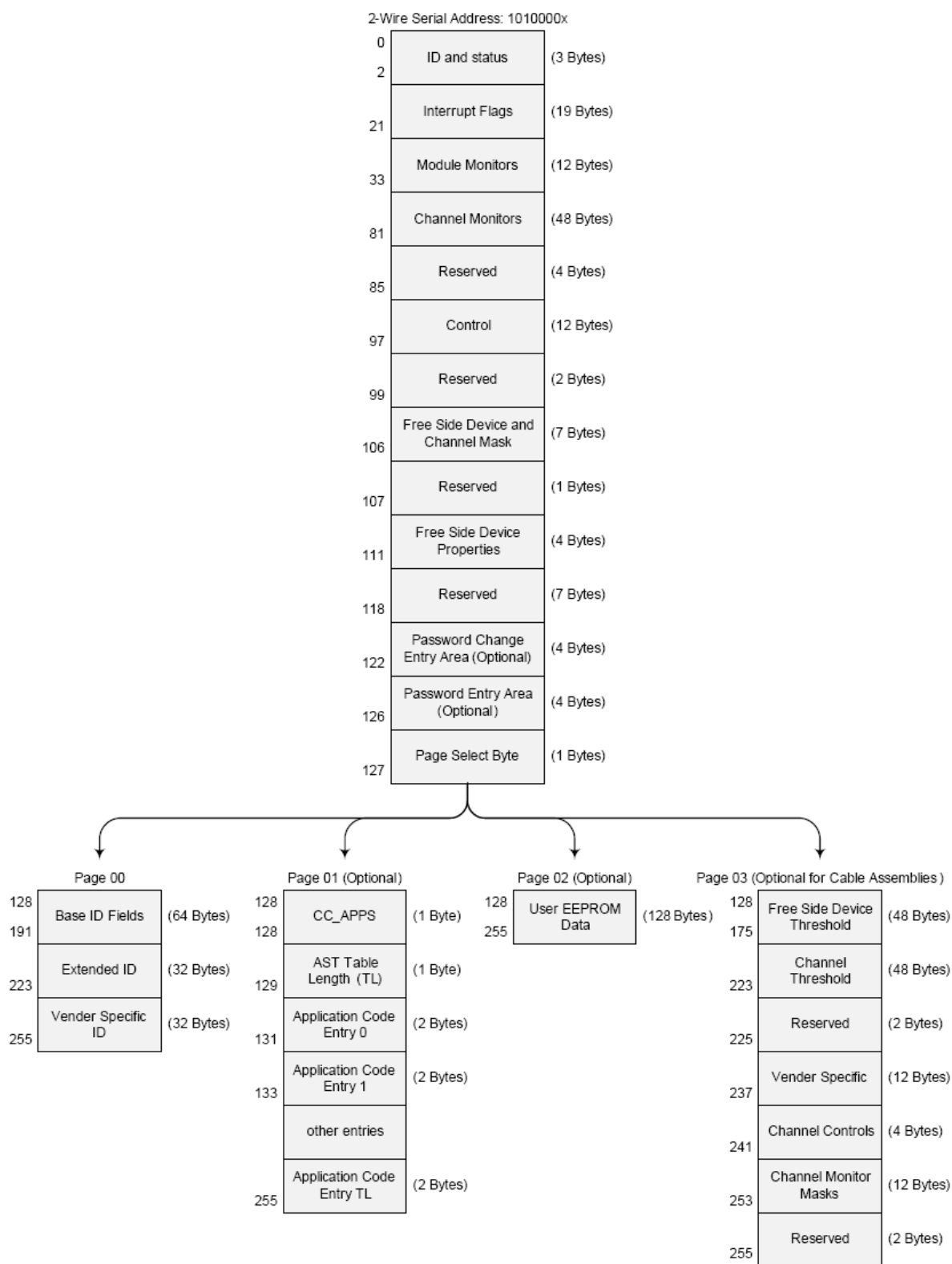
- Transceiver temperature
- Laser bias current (4-Channel)
- Transmitted optical power (4-Channel)
- Received optical power (4-Channel)
- Transceiver supply voltage

It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Controller (DDC) inside the transceiver, which is accessed through the 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL pin) is generated by the host. The positive edge clocks data into the QSFP+ transceiver into those segments of its memory map that are not write-protected. The negative edge clocks data from the QSFP+ transceiver. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially. The 2-wire serial interface provides sequential or random access to the 8 bit parameters, addressed from 000h to the maximum address of the memory.

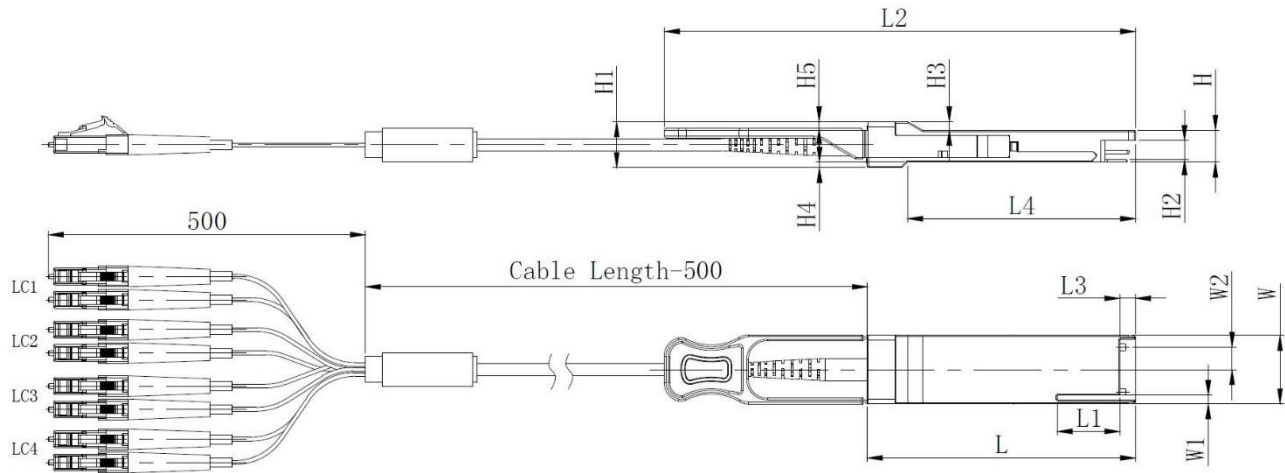
For more detailed information including memory map definitions, please see the QSFP+ MSA Specification.

QSFP+ Digital Diagnostic Memory Map





Mechanical Dimensions



	L	L1	L2	L3	L4	W	W1	W2	H	H1	H2	H3	H4	H5
Max	72.2	-	128	4.35	61.4	18.45	-	6.2	8.6	12.4	5.35	2.5	1.6	2.0
Typical	72.0	-	-	4.20	61.2	18.35	-	-	8.5	12.2	5.2	2.3	1.5	1.8
Min	68.8	16.5	124	4.05	61.0	18.25	2.2	5.8	8.4	12.0	5.05	2.1	1.3	1.6

(All Dimensions are $\pm 0.20\text{mm}$ Unless Otherwise Specified, Unit: mm)

Channel	Optical Interface
Tx1	LC1 Side-A
Rx1	LC1 Side-B
Tx2	LC2 Side-A
Rx2	LC2 Side-B
Tx3	LC3 Side-A
Rx3	LC3 Side-B
Tx4	LC4 Side-A
Rx4	LC4 Side-B

Cable Length (CL)	Tolerance
< 1m	+5 / -0cm
1 ~ 4.5m	+15 / -0cm
5m ~ 14.5m	+30 / -0cm
15m ~ 100m	+2% / -0cm



Ordering Information

FAOC-40G-QPDP--L3

Cable Length

meters

Example: 003=3m, 015=15m, 100=100m, 0X5=0.5m, 3X5=3.5m

Cable Jacket

P: PVC Q: OFNP
L: LSZH* (default)

Fiber Type

2: MM 50/125 OM2
3: MM 50/125 OM3* (default)
4: MM 50/125 OM4