



10G X2-SR Transceiver

Hot Pluggable, Duplex SC, 850nm VCSEL, Multi-Mode, 300M, DDM

Part Number: FXF2-H1-M85-X3D



Overview

FXF2-H1-M85-X3D is a highly integrated serial optical transponder module for 10Gbit/s data transmission that is usable in typical router line card applications, Storage, IP network and LAN and compliant to X2 MSA. The MUX/DEMUX with clock and data recovery (CDR), XAUI interface and MDIO management functions are all integrated into the module. This transceiver uses a 850nm VCSEL Laser Diode to achieve 300m over multi-mode fiber as 10GBASE-SR of the IEEE 802.3ae.

Applications

- 10G Gigabit Ethernet

Features

- XAUI Electrical Interface: 4 Lanes @3.125Gb/s
- Hot Pluggable
- 850nm VCSEL transmitter
- Duplex SC connector
- MDIO Management Interface
- Support DDM function
- Link length up to 300m over Multi mode OM3 fiber
- Compliant to IEEE802.3ae 10GBASE-SR
- Compliant to X2 MSA
- RoHS compliant

Absolute Maximum Ratings

Parameters	Symbol	Min.	Max.	Unit
Storage Temperature	T _{ST}	-40	+85	°C
Storage Relative Humidity	RH	5	95	%
Supply Voltage 3.3V	V _{CC3}	0	+3.6	V
Supply Voltage 1.2V (APS)	V _{CC1}	0	+1.5	V



Recommended Operating Conditions

Parameters	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage 3.3V	V _{CC3}	+3.14	+3.3	+3.47	V
Supply Voltage 1.2V (APS)	V _{CC1}	+1.15	+1.2	+1.25	V
Supply Current 3.3V	I _{CC3}			100	mA
Supply Current 1.2V (APS)	I _{CC1}			1100	mA
Module Total Power Consumption	P _c		1.7	2.4	W
Operating Case Temperature	T _c	0		70	°C

Transmitter Electro-optical Characteristics

V_{CC1}=1.15V to 1.25V, V_{CC3}= 3.14V to 3.47V, T_{OP} = 0 °C to 70 °C

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Data Rate	DR		10.3125		Gb/s	
Signaling speed variation from nominal		-100		+100	ppm	
Maximum Launch Power	P _{MAX}	-3		-1	dBm	
Optical Center Wavelength	λ	840	850	860	nm	
Spectral Width	$\Delta\lambda$		0.4	0.45	nm	
Optical Extinction Ratio	ER	3.5			dB	
Off Transmitter Power	P _{OFF}			-28	dBm	
Dispersion Penalty	DP			3.9	dB	

Receiver Electro-optical Characteristics

V_{CC1}=1.15V to 1.25V, V_{CC3}= 3.14V to 3.47V, T_{OP} = 0 °C to 70 °C

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Data Rate	DR		10.3125		Gb/s	
Receiver Sensitivity (OMA)	SEN			-11.1	dBm	
Optical Center Wavelength	λ	840	850	860	nm	
Stressed Receiver Sensitivity (OMA)	P _{IN-S_min}			-7.5	dBm	
Maximum Receive Power	P _{RX-MAX}	+0.5			dBm	
Receiver Reflectance	R _{rx}			-12	dB	



XAUI I/O Characteristics

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
XAUI Data Rate	DR		3.125		Gb/s	
XAUI Baud Rate Tolerance	P _{IN_min}	-100		+100	dBm	Relative Tolerance
Differential Input Voltage Swing	λ	220		1600	dBm	8B/10B Coded
Differential Output Voltage Swing	P _{IN-S_min}	800		1600	dBm	R _{Load} =100 Ω $\pm$ 5%
Differential Input Impedance	P _{IN_max}	80	100	120	Ω	
Total Output Jitter	R _{rx}			0.35	UI	no pre-equalization
Total Deterministic Output Jitter				0.17	UI	no pre-equalization



Pin Assignment

70	GND	1	GND
69	GND	2	GND
68	RESERVED	3	GND
67	RESERVED	4	5.0V (Not in USE)
66	GND	5	3.3V
65	Tx LANE 3-	6	3.3V
64	Tx LANE 3+	7	APS
63	GND	8	APS
62	Tx LANE 2-	9	LASI
61	Tx LANE 2+	10	RESET
60	GND	11	VEND SPECIFIC
59	Tx LANE 1-	12	Tx ON/OFF
58	Tx LANE 1+	13	RESERVED
57	GND	14	MOD_DET
56	Tx LANE 0-	15	VEND SPECIFIC
55	Tx LANE 0+	16	VEND SPECIFIC
54	GND	17	MDIO
53	GND	18	MDC
52	GND	19	PRTAD4
51	Rx LANE 3-	20	PRTAD3
50	Rx LANE 3+	21	PRTAD2
49	GND	22	PRTAD1
48	Rx LANE 2-	23	PRTAD0
47	Rx LANE 2+	24	VEND SPECIFIC
46	GND	25	APS SET
45	Rx LANE 1-	26	RESERVED
44	Rx LANE 1+	27	APS SENSE
43	GND	28	APS
42	Rx LANE 0-	29	APS
41	Rx LANE 0+	30	3.3V
40	GND	31	3.3V
39	RESERVED	32	5.0V (Not in USE)
38	RESERVED	33	GND
37	GND	34	GND
36	GND	35	GND

Toward Bezel

Top of Transceiver PCB **Bottom of Transceiver PCB
(as view through top)**



Pin Description

PIN	Name	Dir	Logic	Function	Note
1	GND			Electrical Ground	
2	GND			Electrical Ground	
3	GND			Electrical Ground	
4	5.0V			Power	
5	3.3V			Power	
6	3.3V			Power	
7	APS			Adaptive Power Supply	
8	APS			Adaptive Power Supply	
9	LASI	O	1.2V CMOS Open Drain	Link Alarm Status Interrupt, low active, Open Drain Output A pull-up resistor with 10-22KΩ to 1,2V is expected. Logic High: Normal Operation Logic Low: Link Alarm is indicated	
10	Reset	I	1.2V CMOS Open Drain	Low active Reset Input 10KΩ pull-up on Transceiver Logic high = Normal Operation Logic Low = Reset asserted	
11	VEND SPECIFIC			Vendor Specific Pin, leave unconnected	
12	Tx ON/OFF	I	1.2V CMOS Open Drain	High active Transmitter Enable Input 10KΩ pull-up on Transceiver Logic high = Transmitter active (normal Operation) And Register Bit 1.9.0 set to low as well Logic Low = shut down of Transmitter	
13	RESERVED			RESERVED	
14	MOD DETECT	O		1kΩ to Ground On Transceiver	
15	VEND SPECIFIC			Vendor Specific Pin, leave unconnected	
16	VEND SPECIFIC			Vendor Specific Pin, leave unconnected	
17	MDIO	I/O	1.2V CMOS	Management Data I/O. Requires external 10-22 kΩ pull-up to 1.2 V on host.	
18	MDC	I	1.2V CMOS	Management Clock Input	
19	PRTAD4	I		Port Address Bit 4(LOW=0)	
20	PRTAD3	I		Port Address Bit 3(LOW=0)	
21	PRTAD2	I		Port Address Bit 2(LOW=0)	
22	PRTAD1	I		Port Address Bit 1(LOW=0)	
23	PRTAD0	I		Port Address Bit 0(LOW=0)	



24	VEND SPECIFIC			Vendor Specific Pin, leave unconnected	
25	APS SET	I		Feedback Input for APS, Input of APS Setting Resistor	
26	RESERVED			RESERVED	
27	APS SENSE	O		APS Sense Output for APS Control Circuit	
28	APS			Adaptive Power Supply	
29	APS			Adaptive Power Supply	
30	3.3V			Power	
31	3.3V			Power	
32	5.0V			Power	
33	GND			Electrical Ground	
34	GND			Electrical Ground	
35	GND			Electrical Ground	
36	GND			Electrical Ground	
37	GND			Electrical Ground	
38	RESERVED			RESERVED	
39	RESERVED			RESERVED	
40	GND			Electrical Ground	
41	Rx LANE 0+			Module XAUI Output Lane 0+	
42	Rx LANE 0-			Module XAUI Output Lane 0-	
43	GND			Electrical Ground	
44	Rx LANE 1+			Module XAUI Output Lane 1+	
45	Rx LANE 1-			Module XAUI Output Lane 1-	
46	GND			Electrical Ground	
47	Rx LANE 2+			Module XAUI Output Lane 2+	
48	Rx LANE 2-			Module XAUI Output Lane 2-	
49	GND			Electrical Ground	
50	Rx LANE 3+			Module XAUI Output Lane 2+	
51	Rx LANE 3-			Module XAUI Output Lane 2-	
52	GND			Electrical Ground	
53	GND			Electrical Ground	
54	GND			Electrical Ground	
55	Rx LANE 0+			Module XAUI Output Lane 0+	
56	Rx LANE 0-			Module XAUI Output Lane 0-	
57	GND			Electrical Ground	

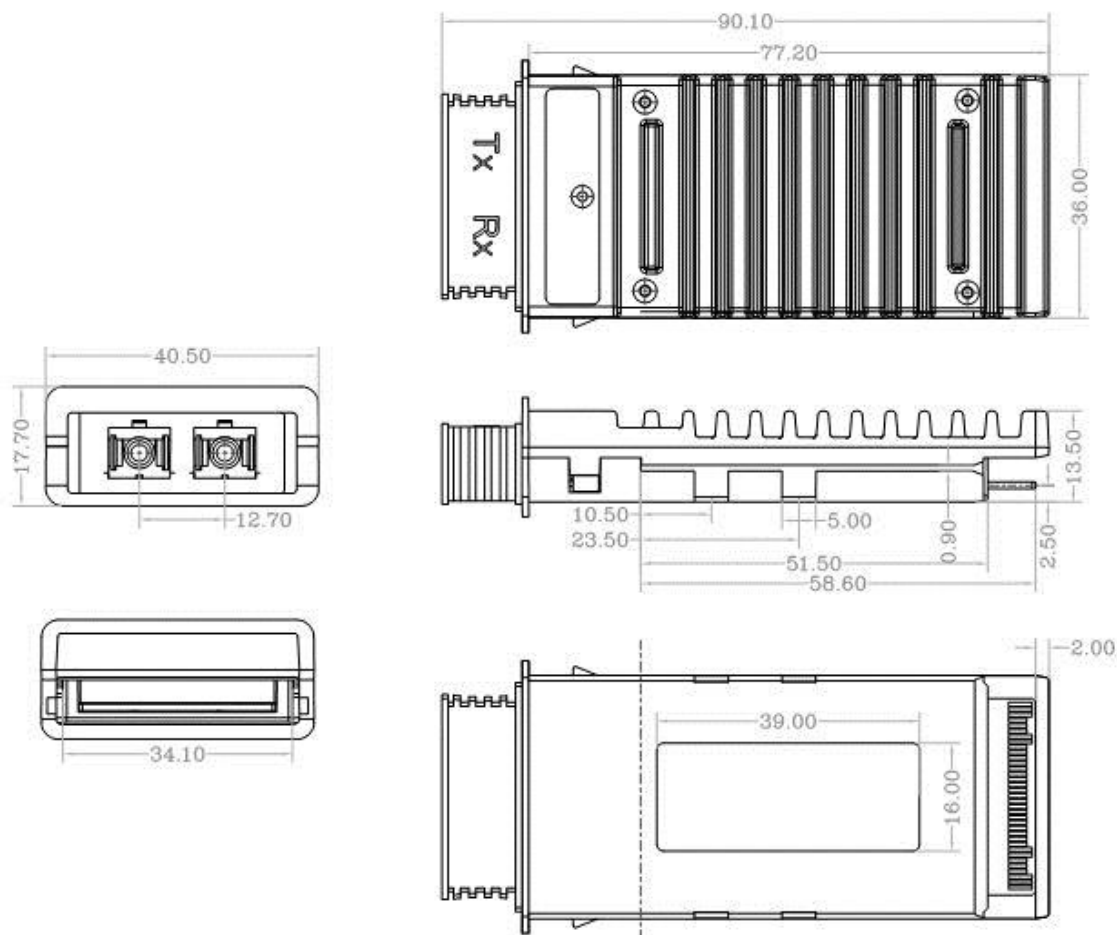


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58	Tx LANE 1+			Module XAUI Output Lane 1+	
59	Tx LANE 1-			Module XAUI Output Lane 1-	
60	GND			Electrical Ground	
61	Tx LANE 2+			Module XAUI Output Lane 2+	
62	Tx LANE 2-			Module XAUI Output Lane 2-	
63	GND			Electrical Ground	
64	Tx LANE 3+			Module XAUI Output Lane 2+	
65	Tx LANE 3-			Module XAUI Output Lane 2-	
66	GND			Electrical Ground	
67	RESERVED			RESERVED	
68	RESERVED			RESERVED	
69	GND			Electrical Ground	
70	GND			Electrical Ground	



Mechanical Dimensions



(All Dimensions are $\pm 0.20\text{mm}$ Unless Otherwise Specified, Unit: mm)

Ordering Information

Part No.	Tx	Rx	Link	DDM	Temp.
FXF2-H1-M85-X3D	850nm	850nm	300m	Yes	0~70°C

Note: Distances are indicative only. To calculate a more precise link budget based on specific conditions in your application, please refer to the optical characteristics.